

MILITARY SPECIFICATION
SEMICONDUCTOR DEVICE, TRANSISTOR, SILICON
TYPE 2N3904

This amendment forms a part of Military Specification
MIL-S-19500/529 (ER), dated 10 May 1978.

PAGE 1

1.3, Maximum Rating table, delete and substitute:

"

P_T	$R_{\theta JC}$	V_{CB}	V_{CE}	V_{EB}	I_C	T_{op} and T_{STG}
$\frac{mW}{350}$	$\frac{^{\circ}C/mW}{125}$	$\frac{V_{dc}}{60}$	$\frac{V_{dc}}{40}$	$\frac{V_{dc}}{6}$	$\frac{mA_{dc}}{200}$	$\frac{^{\circ}C}{-55 \text{ to } +150}$

1/ Derate linearly, 8.0 mW/°C for $T_A > 25^{\circ}C$."

1.4, Primary Electrical Characteristics table: Delete "MAX" and substitute "MIN"; also delete "MIN" and substitute "MAX".

PAGE 2

3.5a.: Delete "or" and substitute "and".

PAGE 12

Figure 1: Delete "C" and substitute "E"; also delete "E" and substitute "C".

Preparing activity:
Army - ER

Agent:
DLA - ES

(Project 5961-A759)